



全漢企業股份有限公司  
FSP TECHNOLOGY INC.

台灣桃園市建國東路22號 統一編號：84239055  
No. 22, Jianguo East Road., Taoyuan City, Taiwan, R.O.C.  
TEL:+886-3-375-9888 Website : www.FSP-group.com  
FAX:+886-3-375-6966 Email : sales@fsp-group.com.tw

# SPECIFICATION



YH-6621B, YH6621-1BAR

Released Date:2013/01/15-17:01:56



## MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION

## TABLE OF CONTENTS

<b>1. PURPOSE .....</b>	<b>3</b>
<b>2. CONCEPTUAL OVERVIEW.....</b>	<b>3</b>
<b>3 DEFINITIONS/TERMS/ACRONYMS.....</b>	<b>3</b>
<b>4. MECHANICAL OVERVIEW .....</b>	<b>4</b>
4.1 TEMPERATURE REQUIREMENTS .....	5
<b>5. AC INPUT REQUIREMENTS .....</b>	<b>5</b>
5.1 AC INLET CONNECTOR .....	5
5.2 REDUNDANT AC INLETS.....	6
5.3 AC INPUT VOLTAGE SPECIFICATION.....	6
5.4 INPUT UNDER VOLTAGE .....	6
5.4.1 Voltage Hold-up Time.....	6
5.5 EFFICIENCY.....	6
5.6 AC LINE DROPOUT.....	7
5.7 AC LINE FUSE.....	7
5.8 AC INRUSH.....	7
5.9 AC LINE TRANSIENT SPECIFICATION.....	7
5.10 AC LINE FAST TRANSIENT SPECIFICATION.....	8
<b>6.DC OUTPUT SPECIFICATION .....</b>	<b>8</b>
6.1 DISTRIBUTION BOARD OUTPUT CONNECTORS .....	8
6.2 GROUNDING .....	11
6.3 REMOTE SENSE.....	11
6.4 OUTPUT POWER/CURRENTS .....	11
6.4.1 Standby Outputs.....	12
6.5 VOLTAGE REGULATION.....	12
6.6 DYNAMIC LOADING .....	12
6.7 CAPACITIVE LOADING .....	12
6.8 RIPPLE / NOISE.....	13
6.9 REDUNDANCY.....	13
6.10 HOT SWAP REQUIREMENTS.....	13
6.11 TIMING REQUIREMENTS.....	14
<b>7. PROTECTION CIRCUITS .....</b>	<b>18</b>
7.1 CURRENT LIMIT.....	18
7.2 OVER VOLTAGE PROTECTION .....	18
<b>8 CONTROL AND INDICATOR FUNCTIONS .....</b>	<b>19</b>
8.1 PSON#.....	19



**MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION**

8.2 PWOK (POWER OK) .....	20
8.3 LED INDICATORS .....	20
9 MTBF .....	21
10 AGENCY REQUIREMENTS.....	21
11 REVISION LOG .....	22

CONFIDENTIAL  
DO NOT COPY



## MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION

### 1. Purpose

This Power Supply Design Guide defines a common redundant power sub-system for use in Pedestal servers and workstation systems. The power sub-system is made up of a cage (with a power distribution board) and hot-swap redundant power modules. This design guide covers the mechanical and electrical requirements of this power sub-system, which is used in a hot-swap redundant configuration. The parameters of this supply are defined in this design guide for open industry use.

### 2. Conceptual Overview

In the Entry server market, the bulk power system must source power on several output rails.

These rails are typically as follows:

- +3.3 V (optional from bulk supply)
- +5 V (optional from bulk supply)
- +12 V
- -12 V
- + 5 V standby

### 3 Definitions/Terms/Acronyms

<b>Required</b>	The status given to items within this design guide, which are required to meet SSI guidelines and a large majority of system applications.
<b>Recommended</b>	The status given to items within this design guide which are not required to meet SSI guidelines, however, are required by many system applications.
<b>Optional</b>	The status given to items within this design guide, which are not required to meet SSI guidelines, however, some system applications may optionally use these features.
<b>Autoranging</b>	A power supply that automatically senses and adjusts itself to the proper input voltage range (110 VAC or 220 VAC). No manual switches or manual adjustments are needed.
<b>CFM</b>	Cubic Feet per Minute (airflow).
<b>Dropout</b>	A condition that allows the line voltage input to the power supply to drop to below the minimum operating voltage.



## MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION

<b>Latch Off</b>	A power supply, after detecting a fault condition, shuts itself off. Even if the fault condition disappears, the supply does not restart unless manual or electronic intervention occurs. Manual intervention commonly includes briefly removing and then reconnecting the supply, or it could be done through a switch. Electronic intervention could be done by electronic signals in the Server System.
<b>Monotonically</b>	A waveform changes from one level to another in a steady fashion, without intermediate retracement or oscillation.
<b>Noise</b>	The periodic or random signals over frequency band of 0 Hz to 20 MHz.
<b>Overcurrent</b>	A condition in which a supply attempts to provide more output current than the amount for which it is rated. This commonly occurs if there is a "short circuit" condition in the load attached to the supply.
<b>PFC</b>	Power Factor Corrected.
<b>Ripple</b>	The periodic or random signals over a frequency band of 0 Hz to 20 MHz.
<b>Rise Time</b>	Rise time is defined as the time it takes any output voltage to rise from 10% to 95% of its nominal voltage.
<b>Sag</b>	The condition where the AC line voltage drops below the nominal voltage conditions.
<b>Surge</b>	The condition where the AC line voltage rises above nominal voltage.
<b>VSB or Standby Voltage</b>	An output voltage that is present whenever AC power is applied to the AC inputs of the supply.
<b>MTBF</b>	Mean time between failure
<b>PWOK</b>	A typical logic level output signal provided by the supply that signals the Server System that all DC output voltages are within their specified range.

#### 4. Mechanical Overview

The ERP2U is a power sub-system made up of a cage and redundant, hot swappable power supply modules. A mechanical drawing of the cage is shown below in Figure 1. The cage is intended to be mounted in the system and not redundant or hot swappable. The exterior face of the cage accepts hot swappable power supply modules. The distribution board within the cage distributes output power from the modules to a wire harness. Cooling fans, EMI filtering, and IEC inlet connector(s) may be located in the modules or cage.

**Dimensions: 101mm (W) × 84mm (H) × 300mm (L)**



## MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION

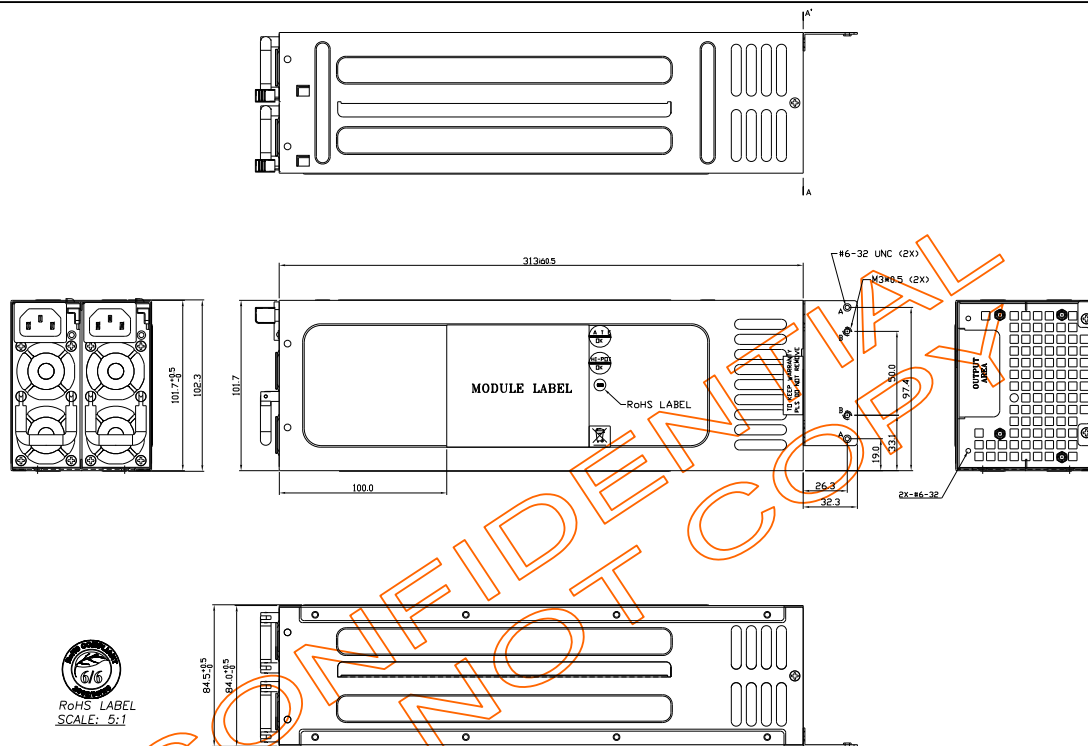


Figure 1: ERP2U Cage Enclosure Drawing

#### 4.1 Temperature Requirements

The operation ambient temperature 350w~620w shall be 0°C to 50°C.

#### 5. AC Input Requirements

The power supply modules shall incorporate universal power input with active power factor correction, which shall reduce line harmonics in accordance with the EN61000-3-2 and JEIDA MITI standards.

##### 5.1 AC Inlet Connector

The AC input connector shall be an IEC 320 C-14 power inlet. This inlet is rated for 15 A/250 VAC.



## MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION

### 5.2 Redundant AC Inlets

The power supply assembly may have dual redundant AC inlets. The power supply shall be able to operate over its full, specified range of requirements with either or both AC input powered. If there is a loss of one AC inlet the power supplies shall continue to operate with no interruption of performance. It is required that all redundant power supply modules be present to support redundant AC inlets.

### 5.3 AC Input Voltage Specification

The power supply must operate within all specified limits over the following input voltage range. Harmonic distortion of up to 10% THD must not cause the power supply to go out of specified limits. The power supply shall operate properly at 85 VAC input voltage to guarantee proper design margins.

**Table 1: AC Input Rating**

PARAMETE	MIN	RATED	MAX
Voltage	90	100-127 V <sub>rms</sub>	140 V <sub>rms</sub>
Voltage	180	200-240 V <sub>rms</sub>	264 V <sub>rms</sub>
Frequency	47 Hz		63 Hz

### 5.4 Input Under Voltage

The power supply shall contain protection circuitry such that application of an input voltage below the minimum specified in section 5.3 shall not cause damage to the power supply.

#### 5.4.1 Voltage Hold-up Time

The power supply hold-up time requirements is specified at 75% of maximum load.

### 5.5 Efficiency

The following table provides required minimum efficiency levels. The "required" minimum efficiency levels are for the purpose of proper power supply cooling when installed in the system. The efficiency is specified at 50% and 20% loading conditions to help reduce system power consumption at typical system loading conditions.

Efficiency shall be tested at AC input voltages of 115VAC and 230VAC. Refer to [www.efficientpowersupplies.org](http://www.efficientpowersupplies.org) for details on proper efficiency testing methods.

**Table 2: Efficiency**

Loading	100% of maximum
Required minimum	65%



## MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION

### 5.6 AC Line Dropout

An AC line dropout is defined to be when the AC input drops to 0 VAC at any phase of the AC line for any length of time. During an AC dropout of one cycle or less the power supply must meet dynamic voltage regulation requirements up to 75% of the rated output load. An AC line dropout of one cycle or less shall not cause any tripping of control signals or protection circuits. If the AC dropout lasts longer than one cycle or the load is greater than 75%, the power supply should recover and meet all turn on requirements. The power supply must meet the AC dropout requirement over rated AC voltages, frequencies, and output loading conditions. Any dropout of the AC line shall not cause damage to the power supply. In the case of redundant AC inputs, the AC line dropout may occur on either or both AC inlet.

### 5.7 AC Line Fuse

The power supply shall incorporate one input fuse on the LINE side for input over-current protection to prevent damage to the power supply and meet product safety requirements. Fuses should be slow blow type or equivalent to prevent nuisance trips. AC inrush current shall not cause the AC line fuse to blow under any conditions. All protection circuits in the power supply shall not cause the AC fuse to blow unless a component in the power supply has failed. This includes DC output load short conditions.

### 5.8 AC Inrush

An additional inrush current limit is recommended for some system applications that require multiple systems on a single AC circuit. After one-quarter of the AC cycle, the input current should be no more than the specified maximum input current.

Module	1+0	1+1
Maximum	80A	160A

### 5.9 AC Line Transient Specification

AC line transient conditions shall be defined as “sag” and “surge” conditions. Sag conditions (also referred to as “brownout” conditions) will be defined as the AC line voltage dropping below nominal voltage. Surge conditions will be defined as the AC line voltage rising above nominal voltage.

The power supply shall meet the requirements under the following AC line sag and surge conditions.

**Table 3: AC Line Sag Transient Performance**

AC Line Sag				
Duration	Sag	Operating AC Voltage	Line Frequency	Performance Criteria
Continuous	10%	Nominal AC Voltage ranges	50/60 Hz	No loss of function or performance
0 to 1 AC cycle	100%	Nominal AC Voltage ranges	50/60 Hz	No loss of function or performance
>1 AC cycle	>10%	Nominal AC Voltage ranges	50/60 Hz	Loss of function acceptable, self recoverable





## MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION

Table 4: AC Line Surge Transient Performance

AC Line Surge				
Duration	Surge	Operating AC Voltage	Line Frequency	Performance Criteria
Continuous	10%	Nominal AC Voltages	50/60 Hz	No loss of function or performance
0 to ½ AC cycle	30%	Mid-point of nominal AC Voltages	50/60 Hz	No loss of function or performance

### 5.10 AC Line Fast Transient Specification

The power supply shall meet the *EN61000-4-5* directive and any additional requirements in *IEC1000-4-5:1995* and the Level 3 requirements for surge-withstand capability, with the following conditions and exceptions:

- These input transients must not cause any out-of-regulation conditions, such as overshoot and undershoot, nor must it cause any nuisance trips of any of the power supply protection circuits.
- The surge-withstand test must not produce damage to the power supply.
- The supply must meet surge-withstand test conditions under maximum and minimum DC-output load conditions.

## 6.DC Output Specification

These are the output requirements for the power supply assembly.

### 6.1 Distribution Board Output Connectors

The power supply distribution board shall have the following output connector and wire harness configuration.



MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION

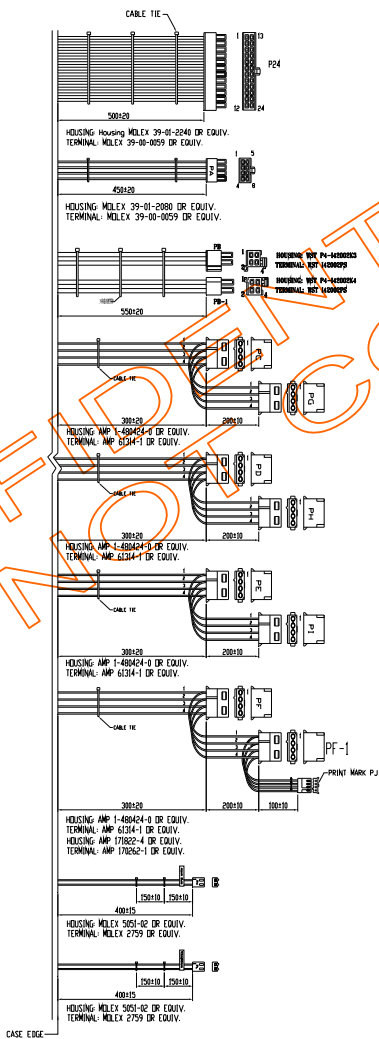


Figure 2: DC Output Connectors



MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION

P24: 24PIN

CONN. NO	RATING	WIRE COLOR	WIRE TYPE
POS. 1	+3.3V	ORANGE	18 AWG
POS. 2	+3.3V	ORANGE	18 AWG
POS. 3	GND	BLACK	18 AWG
POS. 4	+5V	RED	18 AWG
POS. 5	+5VRS	RED	22 AWG
	GND	BLACK	18 AWG
	RS-GND	BLACK	22 AWG
POS. 6	+5V	RED	18 AWG
POS. 7	GND	BLACK	18 AWG
POS. 8	PW-EN	GRAY	22 AWG
POS. 9	+5VSB	PURPLE	18 AWG
POS. 10	+12V	YELLOW	18 AWG
	+12VRS	YELLOW	22 AWG
POS. 11	+12V	YELLOW	18 AWG
POS. 12	+3.3V	ORANGE	18 AWG
POS. 13	+3.3V	ORANGE	18 AWG
POS. 14	+12V	BROWN	22 AWG
POS. 15	GND	BLACK	18 AWG
POS. 16	PS-EN	GREEN	22 AWG
POS. 17	GND	BLACK	18 AWG
POS. 18	GND	BLACK	18 AWG
POS. 19	GND	BLACK	18 AWG
POS. 20			
POS. 21	+5V	RED	18 AWG
POS. 22	+5V	RED	18 AWG
POS. 23	+5V	RED	18 AWG
POS. 24	GND	BLACK	18 AWG

PA: 8PIN

CONN. NO	RATING	WIRE COLOR	WIRE TYPE
POS. 1	GND	BLACK	18 AWG
POS. 2	GND	BLACK	18 AWG
POS. 3	GND	BLACK	18 AWG
POS. 4	GND	BLACK	18 AWG
POS. 5	+12V	YELLOW	18 AWG
POS. 6	+12V	YELLOW	18 AWG
POS. 7	+12V	YELLOW	18 AWG
POS. 8	+12V	YELLOW	18 AWG

PB/PB-I: 4PIN

CONN. NO	RATING	WIRE COLOR	WIRE TYPE
POS. 1	GND	BLACK	18 AWG
POS. 2	GND	BLACK	18 AWG
POS. 3	+12V	YELLOW	18 AWG
POS. 4	+12V	YELLOW	18 AWG

PC/PG: L4P/L4P

CONN. NO	RATING	WIRE COLOR	WIRE TYPE
POS. 1	+12V	YELLOW	18 AWG
POS. 2	GND	BLACK	18 AWG
POS. 3	GND	BLACK	18 AWG
POS. 4	+5V	RED	18 AWG

PD/PH: L4P/L4P

CONN. NO	RATING	WIRE COLOR	WIRE TYPE
POS. 1	+12V	YELLOW	18 AWG
POS. 2	GND	BLACK	18 AWG
POS. 3	GND	BLACK	18 AWG
POS. 4	+5V	RED	18 AWG

PE/PI: L4P/L4P

CONN. NO	RATING	WIRE COLOR	WIRE TYPE
POS. 1	+12V	YELLOW	18 AWG
POS. 2	GND	BLACK	18 AWG
POS. 3	GND	BLACK	18 AWG
POS. 4	+5V	RED	18 AWG

PF/PF-1/PJ: L4P/S4P

CONN. NO	RATING	WIRE TYPE	CONN. NO	WIRE TYPE	WIRE COLOR
POS. 1	+12V	18 AWG	POS. 5	22 AWG	YELLOW
POS. 2	GND	18 AWG	POS. 6	22 AWG	BLACK
POS. 3	GND	18 AWG	POS. 7	22 AWG	BLACK
POS. 4	+5V	18 AWG	POS. 8	22 AWG	RED

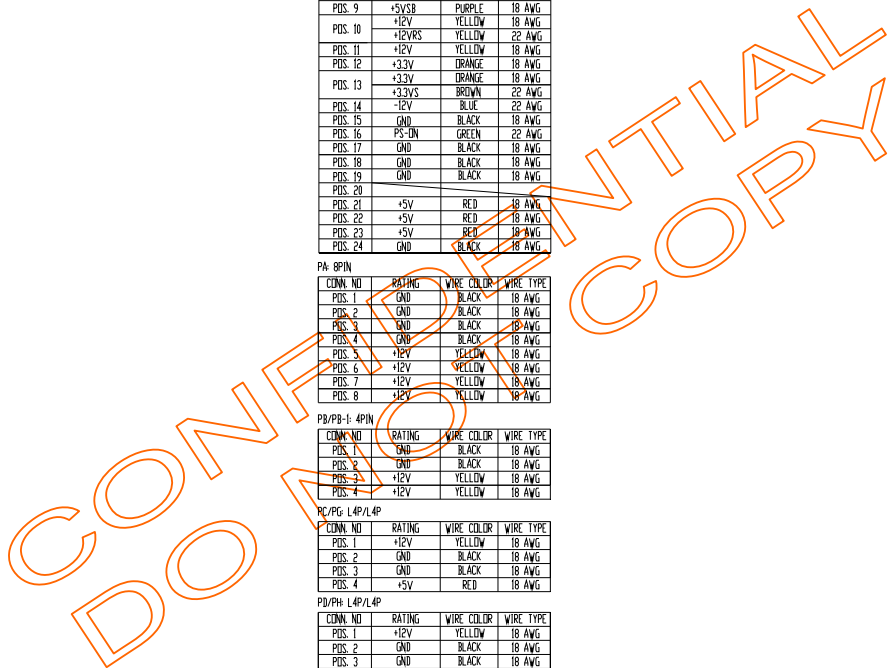
PK: 2P

CONN. NO	RATING	WIRE COLOR	WIRE TYPE
POS. 1	ALARM MUTE(-)	BLACK	24 AWG
POS. 2	ALARM MUTE(+)	YELLOW	24 AWG

PL: 2P

CONN. NO	RATING	WIRE COLOR	WIRE TYPE
POS. 1	TTL SIGNAL(-)	BLACK	24 AWG
POS. 2	TTL SIGNAL(+)	RED	24 AWG

Table 5 DC Output Assignment





## MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION

### 6.2 Grounding

The ground of the pins of the power assembly wire harness provides the power return path. The wire harness ground pins shall be connected to safety ground (power supply enclosure).

### 6.3 Remote Sense

The power assembly may have remote sense for the +3.3V (3.3RS) and return (ReturnS) if the Optional Server Signal connector is implemented and the module has a +3.3V output. The remote sense return (ReturnS) is used to regulate out ground drops for all output voltages. The +3.3V remote sense (3.3RS) is used to regulate out drops in the system for the +3.3 V output. The remote sense input impedance to the power sub-assembly must be greater than 200 ohms on 3.3RS and ReturnS. This is the value of the resistor connecting the remote sense to the output voltage internal to the power assembly. Remote sense must be able to regulate out a minimum of 200 mV drop on the +3.3 V output. The remote sense return (ReturnS) must be able to regulate out a minimum of 200 mV drop in the power ground return. The current in any remote sense line shall be less than 5 mA to prevent voltage sensing errors. The power supply must operate within specification over the full range of voltage drops from the power assembly's output connector to the remote sense points.

### 6.4 Output Power/Currents

The following tables define the power and current ratings for different recommended power levels. Depending upon the system design, the power supply modules may have less outputs than required by the system (example: +12V and 5VSB). If there are less outputs than required by the system on the module, the cage shall have additional DC/DC converters to generate the voltages not produced by the modules (example: +12V/+5V, +12V/+3.3V, +12V/-12V). The combined output power of all outputs from the cage shall not exceed the rated output power. The power assembly shall meet both static and dynamic voltage regulation requirements over the full load ranges. The power sub-assembly shall supply redundant power over the full load ranges.

**Table 6: 620 W Load Ratings**

Voltage	Minimum Continuous	Maximum Continuous	Peak
+3.3 V	0.5 A	25 A	
+5 V	0.5 A	30 A	
+12V	2.0 A	48 A	
-12 V	0 A	0.5 A	
+5 VSB	0 A	4.0 A	

1. Maximum continuous total DC output power should not exceed 620 W.
2. Maximum continuous combined load on +3.3 VDC and +5 VDC outputs shall not exceed 150 W.
3. Maximum peak total DC output power should not exceed 680 W.
4. Peak power and current loading shall be supported for a minimum of 10 second.



## MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION

### 6.4.1 Standby Outputs

The 5 VSB output shall be present when an AC input greater than the power supply turn on voltage is applied.

### 6.5 Voltage Regulation

The power assembly output voltages must stay within the following voltage limits when operating at steady state and dynamic loading conditions. These limits include the peak-peak ripple/noise specified in Section 5.10. All outputs are measured with reference to the return remote sense (ReturnS) signal. The 5V, 12V, -12V and 5 VSB outputs are measured at the power assembly connectors referenced to ReturnS. The +3.3 V is measured at its remote sense signal (3.3VS) located at the signal connector.

**Table 7: Voltage Regulation Limits**

Parameter	MIN	NOM	MAX	Units	Tolerance
+3.3 V	+3.135	+3.30	+3.46	V <sub>rms</sub>	+5/-5%
+5 V	+4.75	+5.00	+5.25	V <sub>rms</sub>	+5/-5%
+12V	+11.40	+12.00	+12.60	V <sub>rms</sub>	+5/-5%
-12 V	-10.80	-12.20	-13.20	V <sub>rms</sub>	+10/-10%
+5 VSB	+4.75	+5.00	+5.25	V <sub>rms</sub>	+5/-5%

### 6.6 Dynamic Loading

The output voltages shall remain within the limits specified in Table 19 for the step loading and within the limits specified in for the capacitive loading. The load transient repetition rate shall be tested between 50 Hz and 5 kHz at duty cycles ranging from 10%-90%. The load transient repetition rate is only a test specification. The . step load may occur anywhere within the MIN load to the MAX load shown in Table 13 or . Table 15

**Table 8: Transient Load Requirements**

Output	. Step Load Size	Load Slew Rate	Capacitive Load
+3.3 V	20% of max load	0.25 A/ $\mu$ s	1000 $\mu$ F
+5 V	20% of max load	0.25 A/ $\mu$ s	1000 $\mu$ F
12V	30% of max load	0.25 A/ $\mu$ s	2200 $\mu$ F
+5 VSB	25% of max load	0.25 A/ $\mu$ s	1 $\mu$ F

### 6.7 Capacitive Loading

The power supply shall be stable and meet all requirements, except dynamic loading requirements, with the following capacitive loading ranges.

Note: Up to 10,000  $\mu$ F of the +12V capacitive loading may be on the +12V1 output.



## MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION

**Table 9: Capacitive Loading Conditions**

Output	MIN	MAX	Units
+3.3 V	10	12,000	$\mu$ F
+5 V	10	12,000	$\mu$ F
+12 V	10	11,000	$\mu$ F
-12 V	1	350	$\mu$ F
+5 VSB	1	350	$\mu$ F

**6.8 Ripple / Noise**

The maximum allowed ripple/noise output of the power supply is defined in . This is measured over a bandwidth of 0 Hz to 20 MHz at the power supply output connectors. A 10  $\mu$ F tantalum capacitor in parallel with a 0.1  $\mu$ F ceramic capacitor are placed at the point of measurement. Table 23

**Table 10: Ripple and Noise**

+3.3 V	+5 V	+12 V	-12 V	+5 VSB
50 mVp-p	50 mVp-p	120 mVp-p	120 mVp-p	50 mVp-p

**6.9 Redundancy**

The power sub-system may have different levels of redundancy depending upon the availability requirements of the system. The Required, Recommended, and Optional items are broken down here. To be redundant each item must be in the hot swap power supply module.

The power sub-system shall have redundancy of the main power converters for the power factor correction stage and the main +12V output.

**6.10 Hot Swap Requirements**

Table 22The power supply modules shall be hot swappable. Hot swapping a power supply is the process of inserting and extracting a power supply from an operating power system. During this process the output voltages shall remain within the limits specified in Table 19 with the capacitive load specified . The hot swap test must be conducted when the sub-system is operating under both static and dynamic conditions. The sub-system shall not exceed the maximum inrush current as specified in section 5.8. The power supply can be hot swapped by the following methods:

AC connecting separately to each module. Up to two power supplies may be on a single AC power source. Extraction: The AC power will be disconnected from the power supply first and then the power



**MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION**

supply is extracted from the sub-system. This could occur in standby mode or powered on mode. Insertion: The module is inserted into the cage and then AC power will be connected to the power supply module.

For power modules with AC docking at the same time as DC. Extraction: The module is extracted from the cage and both AC and DC disconnect at the same time. This could occur in standby or power on mode. No damage or arcing shall occur to the DC or AC contacts which could cause damage. Insertion: The AC and DC connect at the same time as the module is inserted into the cage. No damage to the connector contacts shall occur. The module may power on or come up into standby mode.

Many variations of the above are possible. Supplies need to be compatible with these different variations depending upon the sub-system construction. In general, a failed (off by internal latch or external control) supply may be removed, then replaced with a good power supply, however, hot swap needs to work with operational as well as failed power supplies. The newly inserted power supply may get turned on by inserting the supply into the system or by system management recognizing an inserted supply and explicitly turning it on.

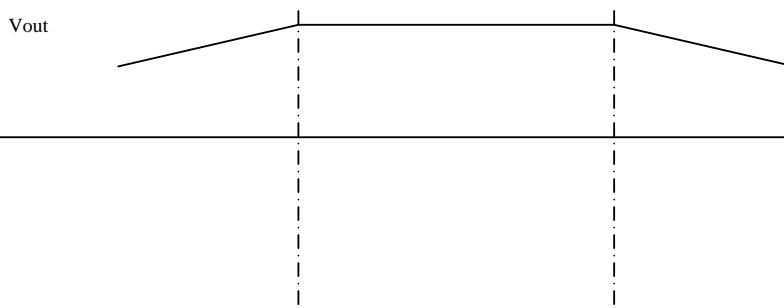
**6.11 Timing Requirements**

These are the timing requirements for the power supply operation. The output voltages must rise from 10% to within regulation limits ( $T_{vout\_rise}$ ) within 1 to 70ms. The +3.3V, +5V and +12V output voltages should start to rise approximately at the same time. All outputs must rise monotonically. The +5V output needs to be greater than the +3.3V output during any point of the voltage rise. The +5V output must never be greater than the +3.3V output by more than 2.25V. Each output voltage shall reach regulation within 50ms ( $T_{vout\_on}$ ) of each other during turn on of the power supply. Each output voltage shall fall out of regulation within 400msec ( $T_{vout\_off}$ ) of each other during turn off. Refer to Figure 2 Power Supply Timing. Figure 3 Turn-on Turn-off Timing shows the timing requirements for the power supply being turned on and off via the AC input with PSON held low, and the power supply being turned on and off with the PSON signal after AC input is applied.

**Table 11 Output Voltage Timing**

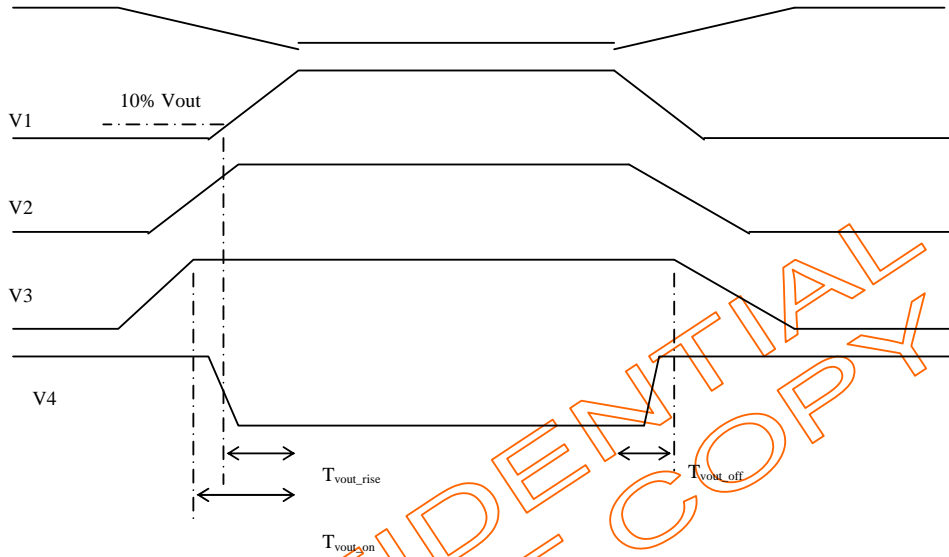
ITEM	DESCRIPTION	MIN	MAX	UNITS
$T_{vout\_rise}$	Output voltage rise time from each main output.	1	70	msec
$T_{vout\_on}$	All main outputs must be within regulation of each other within this time.		50	msec
$T_{vout\_off}$	All main outputs must leave regulation within this time.		400	msec

**Figure 3: Power Supply Timing**





MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION







## MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION

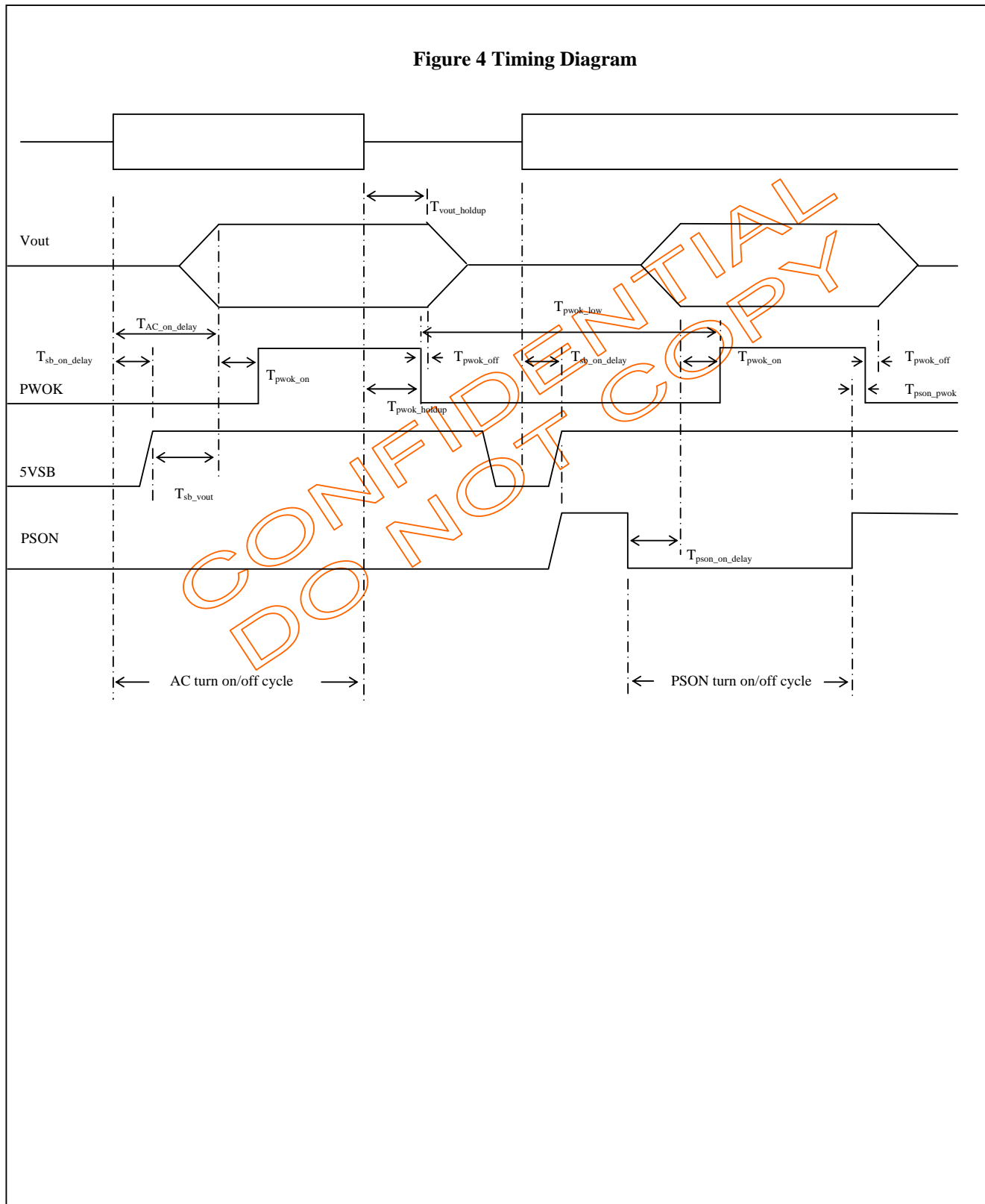
Table 12 Turn On/Turn Off Timing

ITEM	DESCRIPTION	MIN	MAX	UNIS
T <sub>sb_on_delay</sub>	Delay from AC being applied to 5VSB being within regulation.		3000	msec
T <sub>ac_on_delay</sub>	Delay from AC being applied to all output voltages being within regulation.		4500	msec
T <sub>vout_holdup</sub>	Time all output voltages stay within regulation after loss of AC.	17		msec
T <sub>pwok_holdup</sub>	Delay from loss of AC to deassertion of PWOK	16		msec
T <sub>pson_on_delay</sub>	Delay from PSON# active to output voltages within regulation limits.	5	400	msec
T <sub>pson_pwok</sub>	Delay from PSON# deactive to PWOK being deasserted.		100	msec
T <sub>pwok_on</sub>	Delay from output voltages within regulation limits to PWOK asserted at turn on.	100	1000	msec
T <sub>pwok_off</sub>	Delay from PWOK deasserted to output voltages (3.3V, 5V, 12V1, 12V2) dropping out of regulation limits.	1		msec
T <sub>pwok_low</sub>	Duration of PWOK being in the deasserted state during an off/on cycle using AC or the PSON signal.	100		msec
T <sub>sb_vout</sub>	Delay from 5VSB being in regulation to O/Ps being in regulation at AC turn on.	50	1000	msec



MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION

Figure 4 Timing Diagram





## MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION

## 7. Protection Circuits

Protection circuits inside the power supply shall cause only the power supply's main outputs to shutdown. If the power supply latches off due to a protection circuit tripping, an AC cycle OFF for 15 s and a PSON# cycle HIGH for 1 s must be able to reset the power supply.

### 7.1 Current Limit

The power supply shall have current limit to prevent the +3.3 V, +5 V, and +12 V outputs from exceeding the values shown in Table 26. If the current limits are exceeded, the power supply shall shutdown and latch off. The latch will be cleared by toggling the PSON# signal or by an AC power interruption. The power supply shall not be damaged from repeated power cycling in this condition. -12 V and 5 VSB shall be protected under over current or shorted conditions so that no damage can occur to the power supply.

**Table 13: Over Current Protection**

Voltage	Over Current Limit (Iout limit)
+3.3 V	110% minimum; 200% maximum
+5 V	110% minimum; 200% maximum
+12V	110% minimum; 200% maximum

### 7.2 Over Voltage Protection

The power supply over voltage protection shall be locally sensed in the hot swap modules. The power supply shall shutdown and latch off after an over voltage condition occurs. This latch shall be cleared by toggling the PSON# signal or by an AC power interruption. Table 28 contains the over voltage limits. The values are measured at the output of the power supply's connectors. The voltage shall never exceed the maximum levels when measured at the power pins of the power supply connector during any single point of fail. The voltage shall never trip any lower than the minimum levels when measured at the power pins of the power supply connector.

**Table 14: Over Voltage Limits**

Output Voltage	MIN (V)	MAX (V)
+3.3 V	3.9	4.5
+5 V	5.7	6.5
+12V	13.3	14.5



## MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION

## 8 Control and Indicator Functions

The following sections define the input and output signals from the power supply.

Signals that can be defined as low true use the following convention:

$signal\# = \text{low true}$

### 8.1 PSON#

The PSON# signal is required to remotely turn on/off the power supply. PSON# is an active low signal that turns on the +3.3 V, +5 V, +12 V, and -12 V power rails. When this signal is not pulled low by the system, or left open, the outputs (except the +5 VSB and Vbias) turn off. This signal is pulled to a standby voltage by a pull-up resistor internal to the power supply. Refer to Figure 4 for timing diagram.

Table 15: PSON# Signal Characteristic

Signal Type	Accepts an open collector/drain input from the system. Pull-up to VSB located in power supply.	
PSON# = Low, PSKILL = Low	ON	
PSON# = Open, PSKILL = Low or Open	OFF	
PSON# = Low, PSKILL = Open	OFF	
	<b>MIN</b>	<b>MAX</b>
Logic level low (power supply ON)	0V	1.0V
Logic level high (power supply OFF)	2.0V	5.25V
Source current, $V_{pson} = \text{low}$		4mA
Power up delay: $T_{pson\_on\_delay}$	5msec	400msec
PWOK delay: $T_{pson\_pwok}$		50msec

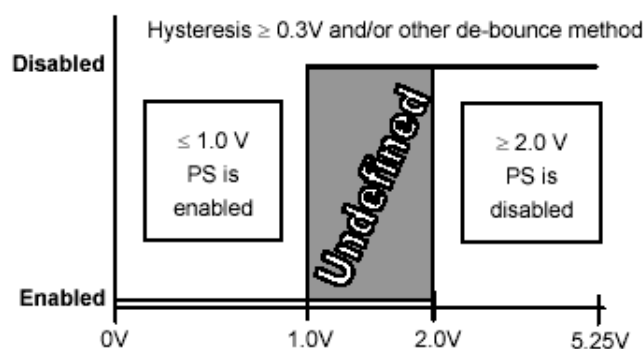


Figure 5: PSON# Signal Characteristics



## MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION

**8.2 PWOK (Power OK)**

PWOK is a power OK signal and will be pulled HIGH by the power supply to indicate that all the outputs are within the regulation limits of the power supply. When any output voltage falls below regulation limits or when AC power has been removed for a time sufficiently long so that power supply operation is no longer guaranteed, PWOK will be deasserted to a LOW state. See *Figure 4* for a representation of the timing characteristics of PWOK. The start of the PWOK delay time shall be inhibited as long as any power supply output is in current limit.

**Table 16: PWOK Signal Characteristics**

<b>Signal Type</b>	Open collector/drain output from power supply. Pull-up to VSB located in power supply.	
<b>PWOK = High</b>	Power OK	
<b>PWOK = Low</b>	Power Not OK	
	<b>MIN</b>	<b>MAX</b>
<b>Logic level low voltage, Isink=4mA</b>	0V	0.4V
<b>Logic level high voltage, Isource=200μA</b>	2.4V	5.25V
<b>Sink current, PWOK = low</b>		4mA
<b>Source current, PWOK = high</b>		2mA
<b>PWOK delay: T<sub>pwok_on</sub></b>	100ms	1000ms
<b>PWOK rise and fall time</b>		100μsec
<b>Power down delay: T<sub>pwok_off</sub></b>	1ms	200msec

**8.3 LED Indicators**

There shall be a single bi-color LED OR two LEDs, one RED and one GREEN, on each hot swap power module to indicate power supply status. When AC is applied to the power supply and standby voltages are available the GREEN LED shall BLINK. The GREEN LED shall turn ON to indicate that all the power outputs are available. The RED LED shall turn ON to indicate that the power supply has failed, shutdown due to over current, or shutdown due to over temperature. Refer to *Table 34: LED Indicators* for conditions of the LED(s).

**Table 17: LED Indicators**

POWER SUPPLY CONDITION	Power Supply LED(s)	
	RED	GREEN
No AC power to all PSU	OFF	OFF
No AC power to this PSU only	OFF	OFF
AC present / Only Standby Outputs On	OFF	OFF
Power supply DC outputs ON and OK	OFF	ON
Power supply failure (includes over voltage, over temperature)	ON	OFF

The LED(s) shall be visible on the power supply's exterior face. The LED location shall meet ESD requirements. LED shall be securely mounted in such a way that incidental pressure on the LED shall not cause it to become displaced.

**MODEL NO: YH6621-1B OPTION AR VERTICAL 1+1 SPECIFICATION****9 MTBF**

---

Using *MIL217F*, the calculated MTBF > 100,000 Hrs at 25° C, nominal input and 75% load conditions.

**10 Agency Requirements**

---

The power supply must comply with all regulatory requirements for its intended geographical market. Depending on the chosen market, regulatory requirements may vary. Although a power supply can be designed for worldwide compliance, there may be cost factors that drive different versions of supplies for different geographically targeted markets. This specification requires that the power supply meet all regulatory requirements for the intended market at the time of manufacturing. Typically this includes:

- UL
- CCC
- CSA
- TUV
- CISPR Class B
- FCC Class B
- The power supply, when installed in the system, shall meet immunity requirements specified in EN55024. Specific tests are to be EN61000-4-2, -3, -4, -5, -6, -8, -11, EN61000-3-2, -3, and JEIDI MITI standard. The power supply must maintain normal performance within specified limits. This testing must be completed by the system EMI engineer. Conformance must be designated with the European Union CE Marking. Specific immunity level requirements are left to customer requirements.

